

AMENDMENTS TO THE SPECIFICATION

Please amend paragraph [0023] as follows:

[0023] The miscellaneous digital data received via line 573 273 may be evaluated in the evaluator unit and transmitted to the controller 257 over the line 277. The controller 257 may be any microcontroller or microprocessor that is capable of responding to data and forming control signals to control other devices. The controller 257 may respond to software or have embedded commands. The data may be saved in a memory 258 according to a specified criterion. The memory 258 may be a mass storage memory device and may include semiconductor, optical, or magnetic memory units or a combination of these devices. The specified criterion for saving the data in the memory may be held in the controller 257. The controller 257 transmits and receives data to and from the memory 258 over the line bus 278. The controller 257 may sort and manage the data within the memory 258. The controller 257 may also perform security, plausibility and/or completeness checks on the digital data. In combination with the memory 258, the controller 257 may form a server unit 259. The controller 257 may be linked to the interface 255 via a bi-directional line bus 279, to the demultiplexer 252 via the line bus 280, and to the DVB-T receiver modules 251 via the line bus 81.